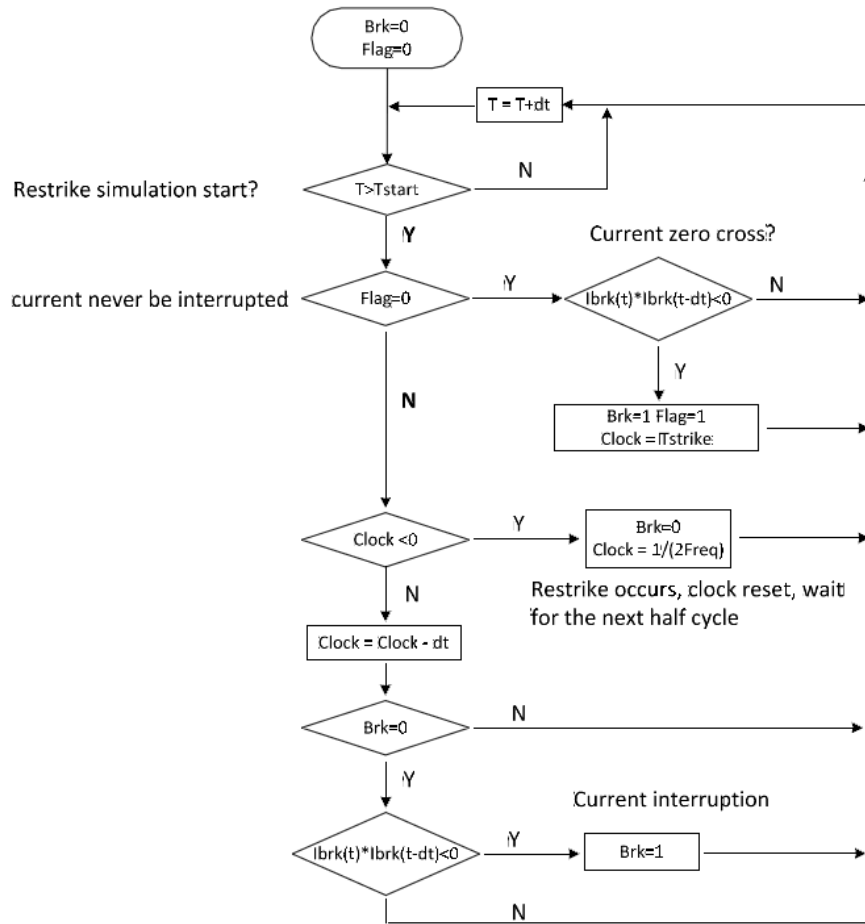


Vacuum Circuit Breaker Restrike



This program supposes that the restrike occurs every half cycle at the instant specified by the Tstrike. When the breaker is close, there is a phase difference between I and V. Whether the voltage is suppressed or escalate depends on the restrike instant. For a capacitive current interruption, the worst scenario is restrike at 180 degree after current zero (when $T_{strike} = 1/(2 * 60) = 0.008333s$).

Where:

Flag: flag to show whether the current be interrupted
 =1 be interrupted
 =0 never be interrupted

Brk: control signal for an ideal breaker
 =1 open immediately at any current
 =0 close

Ibrk: breaker current

Tstart: time to start the restrike simulation, it is not the instant of the current zero cross or restrike.

Tstrike: Tstrike is the interval of the **first** current zero and the **first** restrike.

Clock: timer to determine if the restrike should occur. If $> T_{strike}$ restrike occurs.



DOCUMENT TRACKING

Rev.	Description	Date
0	Initial	
1	Rebranded to new MHI branding guidelines	20/Sep/2018